



PRAIRIE VIEW
A&M UNIVERSITY

Roy G. Perry College of Engineering

HOSTED & ORGANIZED BY
ROY G. PERRY COLLEGE OF
ENGINEERING AT PVAMU

SECURE
CENTER OF EXCELLENCE

PRESENTS

AN INTENSIVE 2-DAY FPGA WORKSHOP

SPARK-FPGA

*Structured Programming and Architecture
for Reconfigurable Knowledge in FPGAs*

LEARN. BUILD. ACCELERATE.



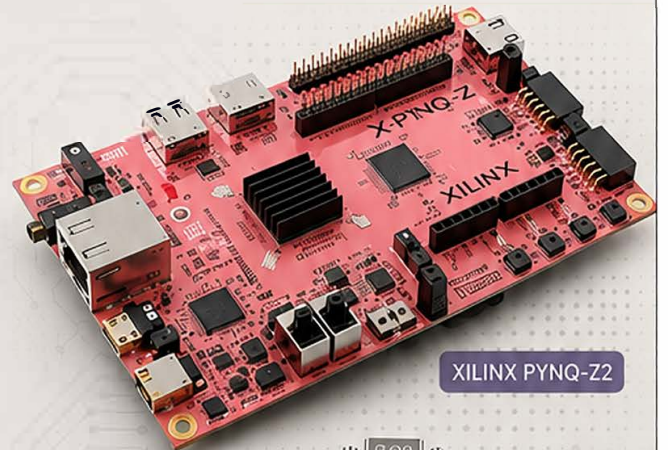
Hands-On
Learning



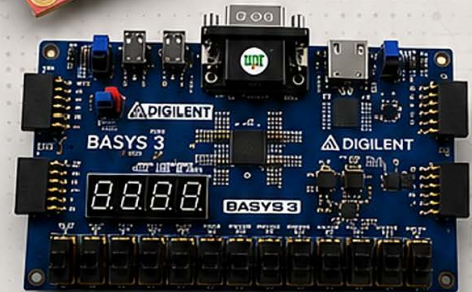
Real-World
Applications



Future-Ready
Skills



XILINX PYNQ-Z2



DIGILENT BASYS 3



DATES

OCTOBER
13 – 14, 2026

(Tuesday - Wednesday)



TIME (CENTRAL TIME)

1:00 PM – 5:00 PM

Daily, both days



VENUE

Maker Space Lab,
EnCARB Building,
College of Engineering,
PVAMU

ABOUT THE WORKSHOP

An intensive, hands-on introduction to FPGA-based digital design — covering FPGA architecture, Verilog/VHDL, simulation, synthesis, timing analysis, and real implementation on FPGA development boards.

KEY TOPICS

- ✓ FPGA Architecture & Design Flow
- ✓ Verilog / VHDL Fundamentals
- ✓ Simulation, Synthesis & Timing Analysis
- ✓ Digital Design Implementations
- ✓ Hands-on Labs with FPGA Boards

FEE
\$300
ONLY!

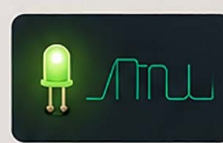
FROM CODE → FPGA → WORKING HARDWARE



1 WRITE VERILOG



2 PROGRAM FPGA



3 LIVE HARDWARE



OPEN TO

FACULTY, ALUMNI,
& INDUSTRY PARTNERS ONLY



INCLUDES

- ✓ Workshop Materials
- ✓ Hands-on Labs
- ✓ Certificate of Participation

RUN BY



SPARK Lab

Security, Privacy, Assurance
Research & Knowledge

EMPOWERING INNOVATION.
SECURING TOMORROW.