

## **CURRICULUM VITA**

### **EDUCATION**

- Ph.D. Computer Science, University of Houston, Houston, TX 2006
- M.S. Computer Science, Southwest Jiaotong University, Chengdu, Sichuan, China, 1997
- B.S. Computer Science, Guizhou University, Guiyang, Guizhou, China, 1994

### **EXPERIENCE** (*List all pervious teaching and administrative positions held relative to Higher Ed.*)

- Prairie View A&M University  
Assistant Professor, 2011 - Present  
Courses: Discrete Structures, Analysis of Algorithms, Data Structures and Algorithms
- University of Houston  
Research Assistant Professor, 2006 - 2011  
Courses: Programming Language and Compilers, Shared Memory Programming

### **PROFESSIONAL, TECHNICAL AND WORK-RELATED EXPERIENCE AND SKILLS**

- Program committee member of International Conference on Parallel Computing (ParCo)
- Program committee member of Workshop on LARGE-SCALE PARALLEL PROCESSING (LSPP)
- Program committee member of International Workshop on OpenMP (IWOMP)

### **PROFESSIONAL ACHIEVEMENTS AND PUBLICATIONS** (*5-7 most recent*)

- Lei Huang and Oscar Hernandez and Wei Ding and Barbara Chapman and Richard Graham, "Towards a High-Level GPU Programming Model (Accepted)", Parallel Computing, 2010
- L. Chen, L. Liu, S. Tang, L. Huang, Z. Jing, S. Xu, D. Zhang, "Unified Parallel C for CPU Clusters: Language Extensions and Compiler Implementation," Proceeding of the Workshops on Languages and Compilers for Parallel Computing (LCPC 2010), Oct. 7-9, Houston, TX
- Lei Huang, Deepak Eachempati, Marcus W. Hervey, and Barbara Chapman. "Exploiting Global Optimizations for OpenMP Programs in the OpenUH Compiler", PPOPP'09,
- Barbara Chapman, Lei Huang, Eric Biscondi, Eric Stotzer, Ashish Shrivastava, and Alan Gatherer, "Implementing OpenMP on a high performance embedded multicore MPSoC", In IPDPS'09: Proceeding of the 2009 IEEE International Symposium on Parallel & Distributed Processing, pages 1-8, Washington, DC, USA, 2009. IEEE Computer Society.
- Barbara Chapman, and Lei Huang, "Enhancing OpenMP and Its Implementation for Programming Multicore Systems", Parallel Computing: Architectures, Algorithms and Applications, volume 38, pp. 3-18, 2008