

Curriculum Vitae

Faculty Name:	Pamela Holland Obiomon		Work Address:	P.O. Box 519; MS 2520 Prairie View, TX 77440
Position Title:	Associate Professor			
Office Location:	New Electrical Engineering Building , Room 330			
Office Phone:	936 261-9907			
Email Address:	phobiomon@pvamu.edu			
	problementopranticeda			
Education:	Degree and Area of Study	Institution	Name	Degree Date
	Ph.D. Electrical Engineering	Texas A&M		August 2003
	M.S. Engineering		A&M University	May 1993
	B.S. Electrical Engineering	University o	f Texas, Arlington	May 1991
Teaching Experience Professional Publications:	Position Title	Institution	Name	Position Dates
	Interim Department Head ECE	Drairia Viau	· A ? M Iniversity	(Beginning and End)
	Interim Department Head ECE Associate/Assistant Professor		A&M University	September 2013 - September 2003 -
	Adjunct Professor		nstitute of Technology	
	Assistant Lecturer	Texas A&M		Summer 1994
			Chirolohy	
	Emmanuel S. Kolawole, Warsame H. Ali, Penrose Cofie, John Fuller, C. Tolliver, Pamela Obiomon, Design and Implementation of Low-Pass, High-Pass and Band-Pass Finite Impulse Response (FIR) Filters Using FPGA, Circuits and Systems, Scientific Research Publishing, Feb. 2015			
	Obiomon, Design and Implementa Impulse Response (FIR) Filters U	ation of Low-Pa	ass, High-Pass and Ba	nd-Pass Finite
	Obiomon, Design and Implementa Impulse Response (FIR) Filters U	ation of Low-Pa Ising FPGA, Cir Sadiku and Par on Substrate w	ass, High-Pass and Ba rcuits and Systems, S mela H. Obiomon, <i>Int</i> <i>ith Finite Element Met</i>	and-Pass Finite cientific Research egrated Circuit hod, Int. Journal of
	Obiomon, Design and Implementa Impulse Response (FIR) Filters U Publishing, Feb. 2015 Sarhan M. Musa, Matthew N. O. S Interconnect Lines on Lossy Silico Engineering Research and Applic	ation of Low-Pa sing FPGA, Cir Sadiku and Par on Substrate w ations, ISSN : 2 embely, Matthe ing Scheduling oplications, Int	ass, High-Pass and Ba rcuits and Systems, So mela H. Obiomon, Int ith Finite Element Met 2248-9622, Vol. 4, Iss ew N. O. Sadiku, and Disciplines on Packe ernational Journal of (and-Pass Finite cientific Research egrated Circuit hod, Int. Journal of ue 1(Version 5), January Pamela H. Obiomon, t Delivery for Next
Additional	Obiomon, Design and Implementa Impulse Response (FIR) Filters U Publishing, Feb. 2015 Sarhan M. Musa, Matthew N. O. S Interconnect Lines on Lossy Silica Engineering Research and Applic 2014, pp.17-21 Sarhan M. Musa, Mahamadou Te Modeling and Simulation of Queu Generation Internet Streaming Ap	ation of Low-Pa sing FPGA, Cir Sadiku and Par on Substrate w ations, ISSN : : embely, Matthe ing Scheduling oplications, Int Volume 03 – Is A., The Importa	ass, High-Pass and Ba rcuits and Systems, So mela H. Obiomon, Int ith Finite Element Met 2248-9622, Vol. 4, Iss ew N. O. Sadiku, and <i>Disciplines on Packe</i> ernational Journal of G ssue 02, March 2014	and-Pass Finite cientific Research egrated Circuit hod, Int. Journal of ue 1(Version 5), January Pamela H. Obiomon, t Delivery for Next Computer and Information
Additional Trainings/Skills:	Obiomon, Design and Implementa Impulse Response (FIR) Filters U Publishing, Feb. 2015 Sarhan M. Musa, Matthew N. O. S Interconnect Lines on Lossy Silica Engineering Research and Applic 2014, pp.17-21 Sarhan M. Musa, Mahamadou Te Modeling and Simulation of Queu Generation Internet Streaming Ap Technology (ISSN: 2279 – 0764) Obiomon, P., Frizell, S., Holland, Classroom, IEEE Potentials Maga	ation of Low-Pa sing FPGA, Cir Sadiku and Par on Substrate w ations, ISSN : 3 embely, Matthe ing Scheduling oplications, Int Volume 03 – Is A., The Importa	ass, High-Pass and Ba rcuits and Systems, So mela H. Obiomon, Int ith Finite Element Met 2248-9622, Vol. 4, Iss ew N. O. Sadiku, and <i>Disciplines on Packe</i> ernational Journal of G ssue 02, March 2014	and-Pass Finite cientific Research egrated Circuit hod, Int. Journal of ue 1(Version 5), January Pamela H. Obiomon, t Delivery for Next Computer and Information
	Obiomon, Design and Implementa Impulse Response (FIR) Filters U Publishing, Feb. 2015 Sarhan M. Musa, Matthew N. O. S Interconnect Lines on Lossy Silica Engineering Research and Applic 2014, pp.17-21 Sarhan M. Musa, Mahamadou Te Modeling and Simulation of Queu Generation Internet Streaming Ap Technology (ISSN: 2279 – 0764) Obiomon, P., Frizell, S., Holland,	ation of Low-Pa sing FPGA, Cir Sadiku and Par on Substrate w ations, ISSN : 3 embely, Matthe ing Scheduling oplications, Int Volume 03 – Is A., The Importa	ass, High-Pass and Ba rcuits and Systems, So mela H. Obiomon, Int ith Finite Element Met 2248-9622, Vol. 4, Iss ew N. O. Sadiku, and <i>Disciplines on Packe</i> ernational Journal of G ssue 02, March 2014	and-Pass Finite cientific Research egrated Circuit hod, Int. Journal of ue 1(Version 5), January Pamela H. Obiomon, t Delivery for Next Computer and Information